



#4/Electrinc
H-1013

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. ISHIDA et al

Serial No. 09/964,484

Examiner: A. Williams

Filed: September 28, 2001

Group Art Unit: 2826

For: A SEMICONDUCTOR DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Restriction Requirement mailed
August 23, 2002, Applicants hereby elect claims 1-31 with
traverse. In addition, Applicants elect all of Figs. 1-24.

It is submitted that at least claim 1 is generic to the
species shown in Figs. 1-24. Claim 1 recites a semiconductor
device comprising first and second semiconductor chips with a
control circuit and a memory circuit provided thereon, first
and second leads, first, second and third bonding wires and a
resin seal member. This basic structure is consistent through
Figs. 1-24. Therefore, it is requested that all of the claims
be examined in light of all of the figures.

In the outstanding Office Action, the Examiner has
requested an election of "any of the species in figures 1 to
24" but has not identified any different species. In the

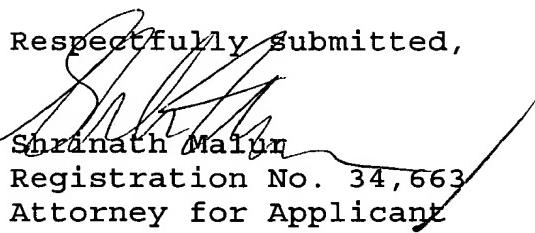
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John H

event the Examiner refuses to examine all of the claims as requested, the Examiner is hereby requested to clearly identify the different species among which an election should be made.

Respectfully submitted,


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